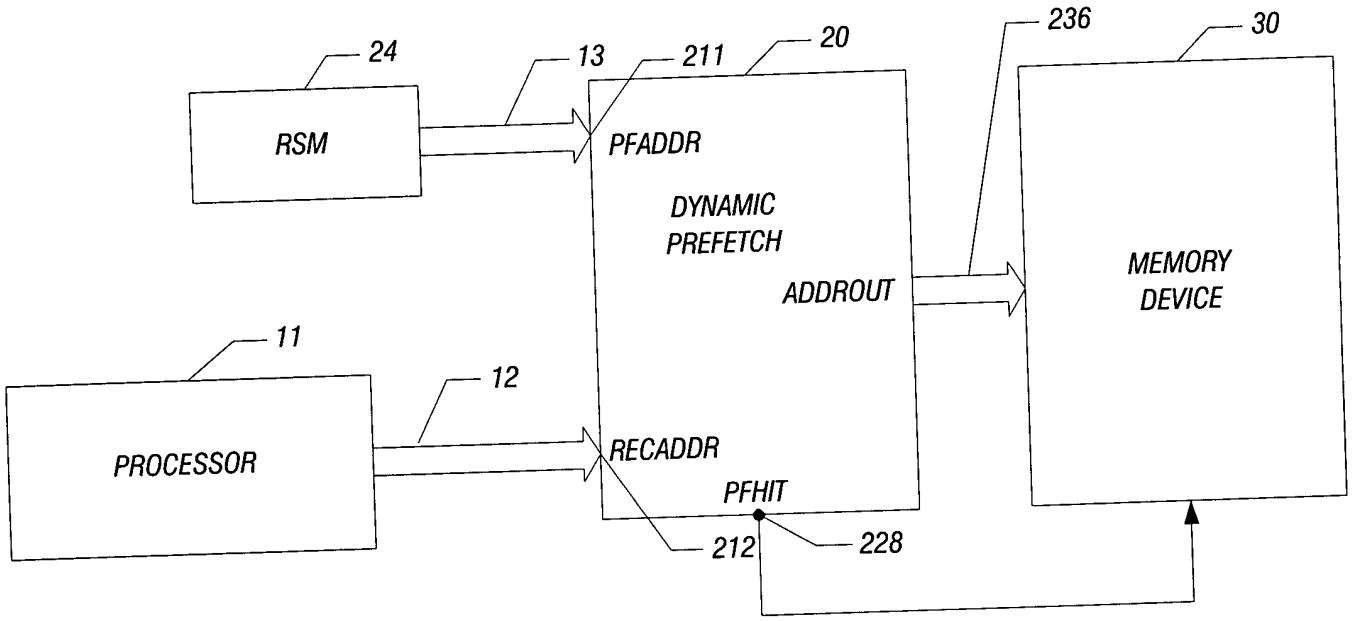
24
13
211
20
236
30
RSM
PFADDR
DYNAMIC
PREFETCH
ADDROUT
MEMORY
DEVICE
11
12
PROCESSOR
RECADDR
PFHIT
212
228

FIG. 1

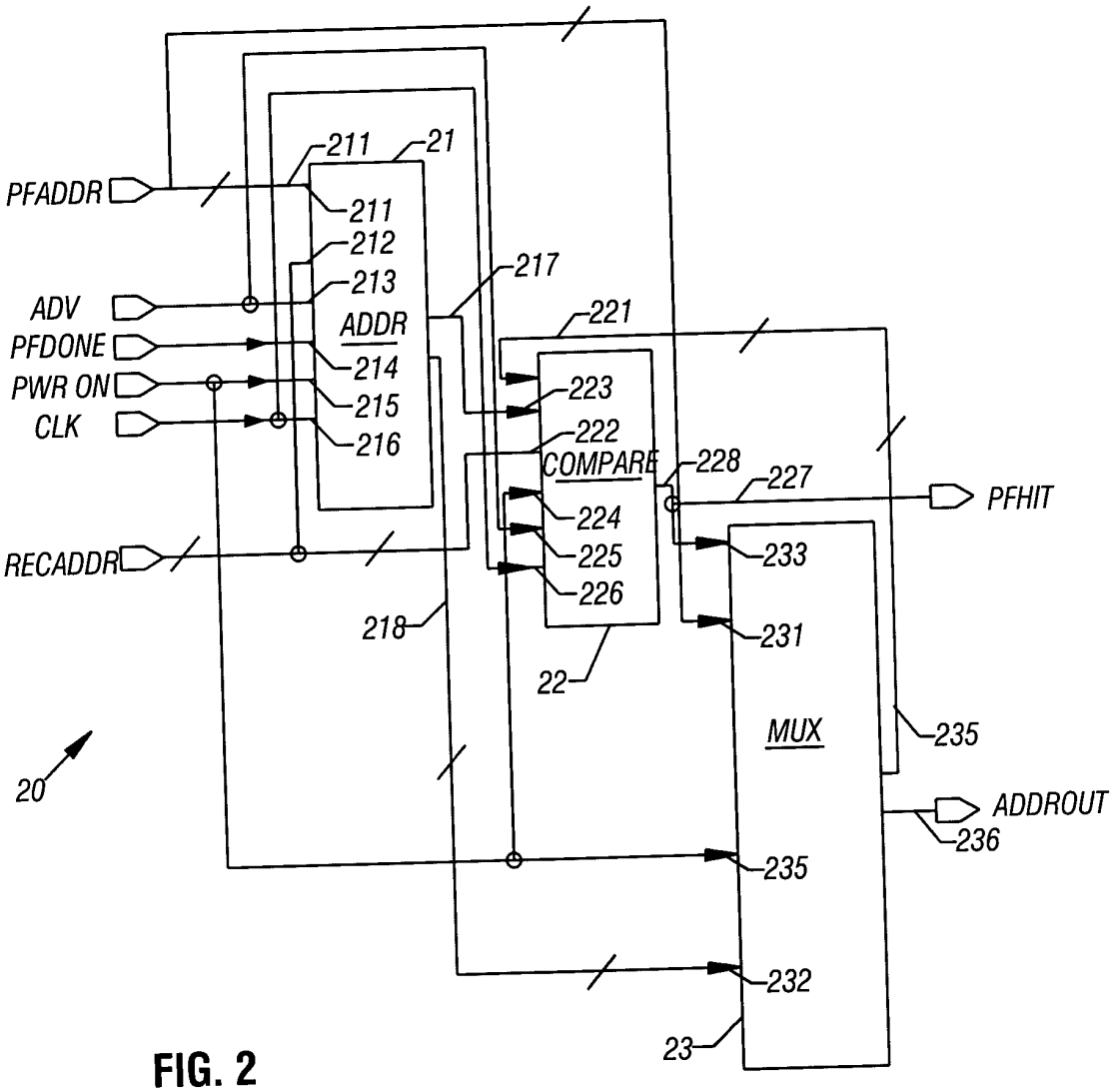
PFADDR
ADV
PFDONE
PWR ON
CLK
RECADDR
211
21
211
212
213
ADDR
214
215
216
217
221
223
222
COMPARE
224
225
226
228
227
PFHIT
233
231
218
22
MUX
235
ADDROUT
236
235
232
23
20

FIG. 2

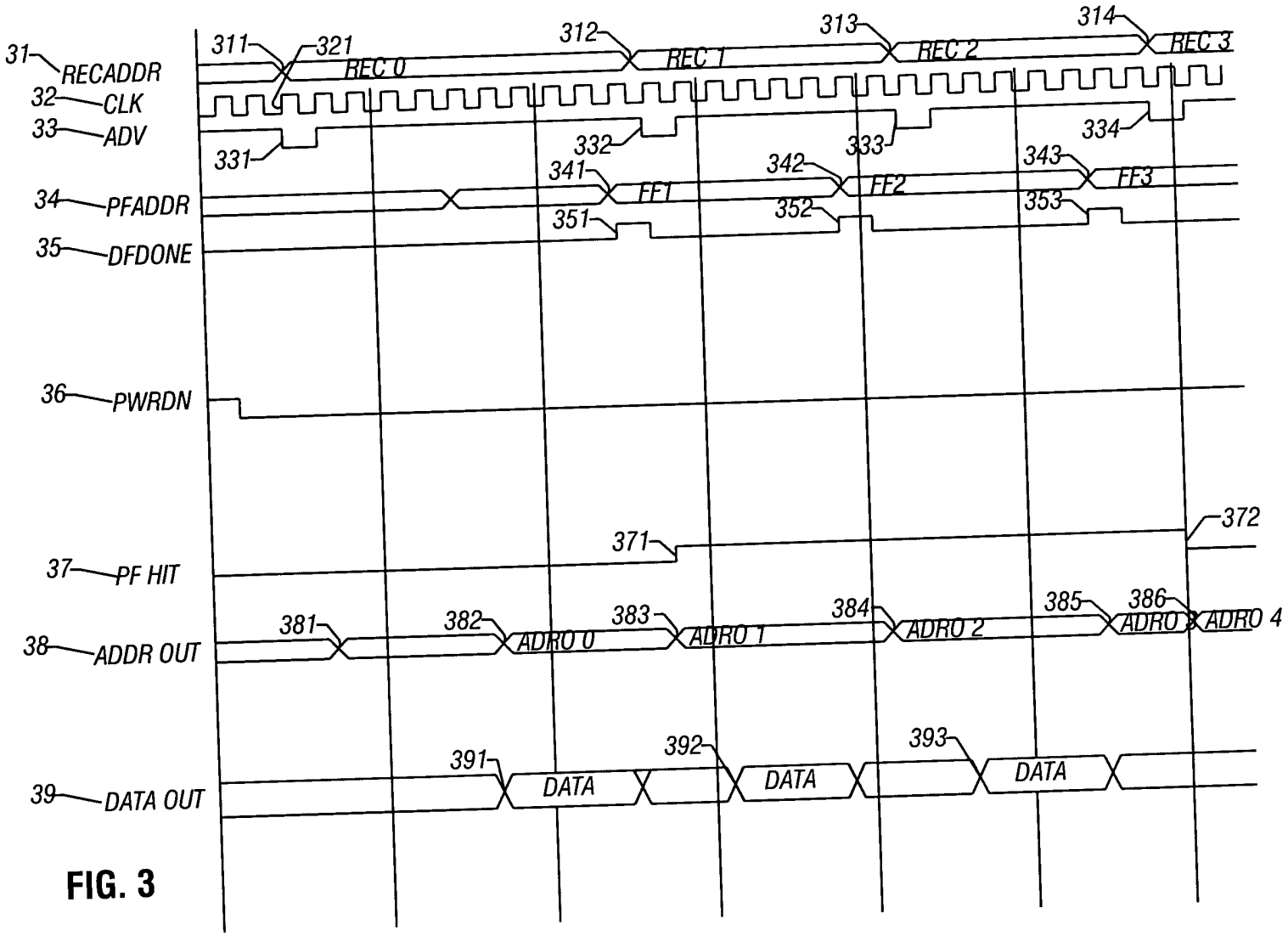
31
RECADDR
32
CLK
33
ADV
34
PFADDR
35
DFDONE
36
PWRDN
37
PF HIT
38
ADDR OUT
39
DATA OUT
311
321
REC 0
312
REC 1
313
REC 2
314
REC 3
331
332
333
334
341
FF1
342
FF2
343
FF3
351
352
353
371
372
381
382
ADR0 0
383
ADR0 1
384
ADR0 2
385
386
ADR0 3
ADR0 4
391
DATA
392
DATA
393
DATA

FIG. 3

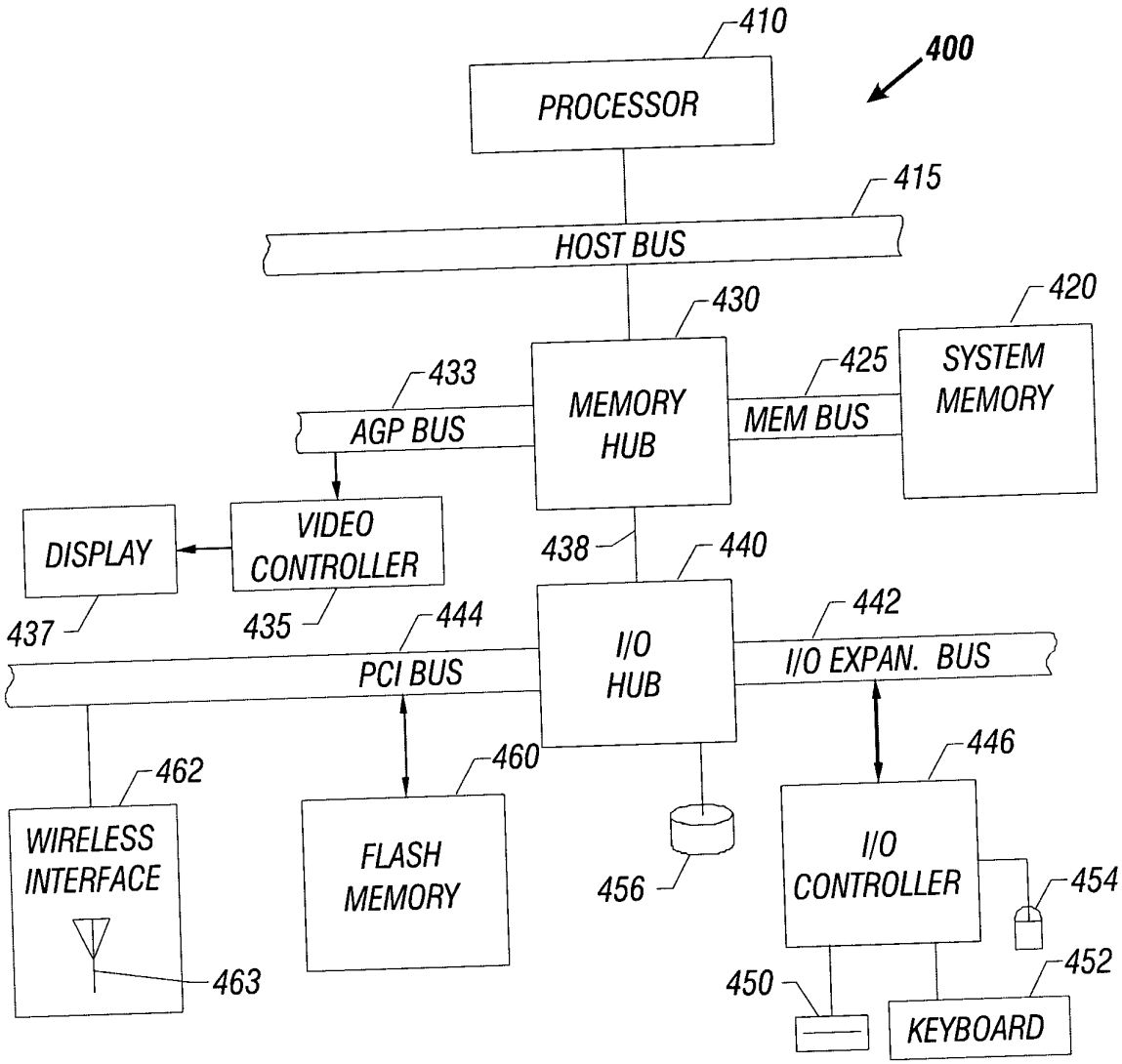
410
400
PROCESSOR
415
HOST BUS
430
420
433
425
SYSTEM
MEMORY
AGP BUS
MEMORY
HUB
MEM BUS
DISPLAY
VIDEO
CONTROLLER
438
440
437
435
444
442
I/O
HUB
PCI BUS
I/O EXPAN. BUS
462
460
446
WIRELESS
INTERFACE
FLASH
MEMORY
456
I/O
CONTROLLER
454
463
450
452
KEYBOARD

FIG. 4